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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Haurie, et al.
Serial No.: 09/575,562
Filed: May 21, 2000
For: METHOD AND APPARATUS FOR USE IN
SWITCHED CAPACITOR SYSTEM

Examiner: Phan, T.
Art Unit: 2818

Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER
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G. Stealy
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AMENDMENT

Sir:

In response to the Office Action mailed February 15, 2002, please amend the above-identified application as follows.

In the Specification

Please replace the paragraph beginning on line 28, page 9, with the following:

FIG. 1 is a block diagram of one embodiment of a portion of a handset 50 for a mobile communication system. The handset 50 includes an input portion having a transducer 54 that receives an input signal 56, e.g., a voice or other acoustical signal, representing information to be communicated via the mobile communication system. The transducer 54 converts the input signal 56 into an electrical signal, typically an analog signal, which is supplied to an analog-to-digital converter (ADC) 58, for example a voiceband ADC. The ADC 58 periodically samples the electrical signal and generates a sequence of multi-bit digital signals, which are supplied to a digital baseband processor 60. The baseband processor 60 performs further signal processing, including for example, compression. The output of the baseband processor 60 is supplied to burst store stage 62, which feeds a GMSK modulator 64. The GMSK modulator 64 produces multi-bit digital signals, which is supplied via signal lines, represented by a signal line 66, to a digital to analog conversion system 68. The digital to analog conversion system 68 converts the sequence of multi-bit digital signals into an analog signal, which is supplied via signal line 70 to

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